

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: THOMAS TEUSCHLER ET AL. - 1

FOR: POLISHED SEMICONDUCTOR WAFER AND PROCESS FOR
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INFORMATION DISCLOSURE STATEMENT

MAIL STOP PATENT APPLICATIONS

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants wish to bring to the attention of the Patent Examiner the reference listed on the enclosed Form PTO-1449 and attached thereto. This reference was cited in the specification on page 2. Since the instant Information Disclosure Statement (IDS) is being filed concurrently with the application, no official fee is required in connection with the same. It is respectfully requested that the foregoing IDS be incorporated into the official file of the concurrently-filed patent application.

Respectfully submitted,
THOMAS TEUSCHLER ET AL. - 1



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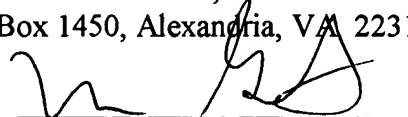
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Enclosures: Form PTO-1449 and one (1) reference

EXPRESS MAIL NO. EV 413 366 139 US

Date of Deposit: January 21, 2004

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Maria Guastella

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. :

TEUSCHLER ET AL.

SERIAL NO.

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT: THOMAS TESCHLER ET AL.

(Use several sheets if necessary)

FILING DATE:

GROUP:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL	1 119 031	01/2001	Europe				
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	AR		
	AS		
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EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.